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writing said data to said memory device,

wherein said data circuit passes said data directly to said memory device in a horizontal mode and

wherein said step of passing said data further comprises:

outputting each bit of said plurality of data bits from said data circuit on a different data bus associated with said memory device; and

wherein said step of writing said data further comprises writing said each bit of said plurality of data bits to a location in said memory device associated with a different address.

43. The method according to claim 41, wherein said step of outputting further comprises:

passing each bit of said plurality of data bits through a respective register.